

\*L1: (1) ("6426254").PN.  
 \*L2: (4) ("6,426,254") or ("6,740,555") or ("6,740,555").PN.  
 \*L3: (4555) deep near6 trench  
 \*L4: (2326) 3 and (upper and lower)  
 \*L5: (259) 4 and (buried adj plate)  
 \*L6: (235) 5 and trench.clm.  
 \*L7: (149) 6 and (plate.clm. or (deep adj trench.clm.)  
 \*L8: (146) 7 and (doped or doping or dopant)  
 \*L9: (97) 8 and (etch\$3.clm.)  
 \*L10: (72) 9 and ((deep adj trench).clm. or (deep adj trench).clm.)  
 \*L11: (75) 10 and (deep adj trench).clm.

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1. **प्रस्तावना**

10 and (deep ad) trench) : c]m-

Author	Year	Site	Age	Sex
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	U	I	PT	P	Document ID	Issue Date	Pages	Title	Current OR	Current KR	Retrieval	Inventor	S	C	P	A	Q
36					US 6566190	20030520	15	Vertical internally-connected trench capacitor	438/242	257/E21.65		Lee, Brian S. et al.					
37					US 6555430	20030429	10	Process flow for capacitance enhancement	438/243	438/212, 438/245, 438/243, 438/244, 438/253		Chudzick, Michael P. et al.					
38					US 6534376	20030319	8	Process flow for sacrificial collar scheme	438/243	257/E21.65		Tews, Helmut Horst					
39					US 6528367	20030304	16	Self-aligned active array along the length	438/253	257/298, 438/238, 257/E21.65		Lee, Brian					
40					US 6518119	20030211	8	Strap with intrinsically conductive integrated circuit	438/243	257/E21.65		Gambino, Jeffrey P. et al.					
41					US 6486024	20021126	10	Trench device with a diode	438/249	257/E21.65		Tews, Helmut Horst et al.					
42					US 6475859	20021105	4	Plasma doping for DRAM with deep trenches and Method for manufacture of improved deep trench	438/243	257/E21.14		Tews, Helmut Horst et al.					
43					US 6452224	20020917	36	Method for manufacture of improved deep trench	257/296	257/301, 257/303, 257/E21.65		Mandelman, Jack A. et al.					
44					US 6440872	20020827	31	Method for hybrid DRAM cell utilizing confined	438/745	257/E21.65		Mandelman, Jack A. et al.					
45					US 6440792	20020827	11	DRAM technology of storage node formation	438/243	438/249, 438/386, 257/296		Shiao, Jia S. et al.					
46					US 6437381	20020820	10	Semiconductor memory device with reduced noise	257/296	257/297, 257/298, 257/E21.55		Gruening, Ulrike et al.					
47					US 6426253	20020730	19	Method of forming a vertically oriented device	438/243	257/E21.55		Tews, Helmut Horst et al.					
48					US 6426251	20020730	8	Process for manufacturing a crystal	438/242	257/301, 257/302, 257/E21.65		Bronner, Gary et al.					
49					US 6417063	20020709	20	Folded deep trench capacitor and method	438/386	257/E21.65		Petter, Robert et al.					
50					US 6391706	20020521	16	Method for making deep trench capacitors for DRAM	438/243	438/244, 438/248, 257/301, 257/302, 438/249		Wu, Chao-Chueh et al.					
51					US 6376324	20020423	12	Collar process for reduced deep trench edge	438/386	257/301, 257/302, 438/249		Mandelman, Jack A. et al.					
52					US 6365485	20020402	14	DRAM technology of	438/392	438/249		Shiao, Jia S. et al.					